

# FORM 4

## UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

OMB APPROVAL

OMB Number: 3235-0287  
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Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

### STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Responses)

1. Name and Address of Reporting Person * Papermaster Mark D		2. Issuer Name and Ticker or Trading Symbol ADVANCED MICRO DEVICES INC [AMD]		5. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director _____ 10% Owner _____ <input checked="" type="checkbox"/> Officer (give title below) _____ SVP & Chief Technology Officer					
(Last) (First) (Middle) ADVANCED MICRO DEVICES, INC., ONE AMD PLACE		3. Date of Earliest Transaction (Month/Day/Year) 07/15/2016							
(Street) SUNNYVALE, CA 94088-3453		4. If Amendment, Date Original Filed(Month/Day/Year)		6. Individual or Joint/Group Filing(Check Applicable Line) <input checked="" type="checkbox"/> Form filed by One Reporting Person <input type="checkbox"/> Form filed by More than One Reporting Person					
(City) (State) (Zip)		<b>Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned</b>							
1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)		4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)
			Code	V	Amount	(A) or (D)			

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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**Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned**  
(e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)		5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4, and 5)	6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Underlying Securities (Instr. 3 and 4)	8. Price of Derivative Security (Instr. 5)	9. Number of Derivative Securities Beneficially Owned Following Reported Transaction(s) (Instr. 4)	10. Ownership Form of Derivative Security: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)	
				Code	V		(A)	(D)						Date Exercisable
RSU Award	(1)	07/15/2016		A		98,572	(2)	(2)	Common Stock	98,572.00	\$ 0	98,572	D	
PRSU Award	(3)	07/15/2016		A		197,145	(4)	(4)	Common Stock	197,145.00	\$ 0	197,145	D	

## Reporting Owners

Reporting Owner Name / Address	Relationships			
	Director	10% Owner	Officer	Other
Papermaster Mark D ADVANCED MICRO DEVICES, INC. ONE AMD PLACE SUNNYVALE, CA 94088-3453			SVP & Chief Technology Officer	

## Signatures

/s/ Mark Papermaster

07/18/2016

\*Signature of Reporting Person

Date

## Explanation of Responses:

\* If the form is filed by more than one reporting person, see Instruction 4(b)(v).

\*\* Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

(1) Each restricted stock unit ("RSU") represents a contingent right to receive one share of AMD's common stock.

(2) The RSUs will vest 1/3 each on 8/9/2017, 8/9/2018 and 8/9/2019.

(3) Each performance-based restricted stock unit ("PRSU") represents a contingent right to receive between 0% and 250% of one share of AMD's common stock. The resulting number of shares acquired upon vesting of the PRSU is contingent upon three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained with the three-year

performance period commencing on July 15, 2016 and ending on August 9, 2019 (the "Performance Period").

The number of PRSUs that may be earned, if at all, is based on three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained within the Performance Period, with the potential payout levels of PRSUs at 50%, 100%, 150%, 200% and 250% of the target number of PRSUs granted. Any PRSU earned pursuant to the attainment of a (4) performance level will vest and be paid out 50% upon the Compensation Committee's certification of the attainment of the performance level (provided, that no PRSU will vest before the first anniversary of the grant date) and the remaining 50% will vest and be paid out at the end of the Performance Period, subject to the recipient's continuous employment or service through each such vesting date.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure.

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